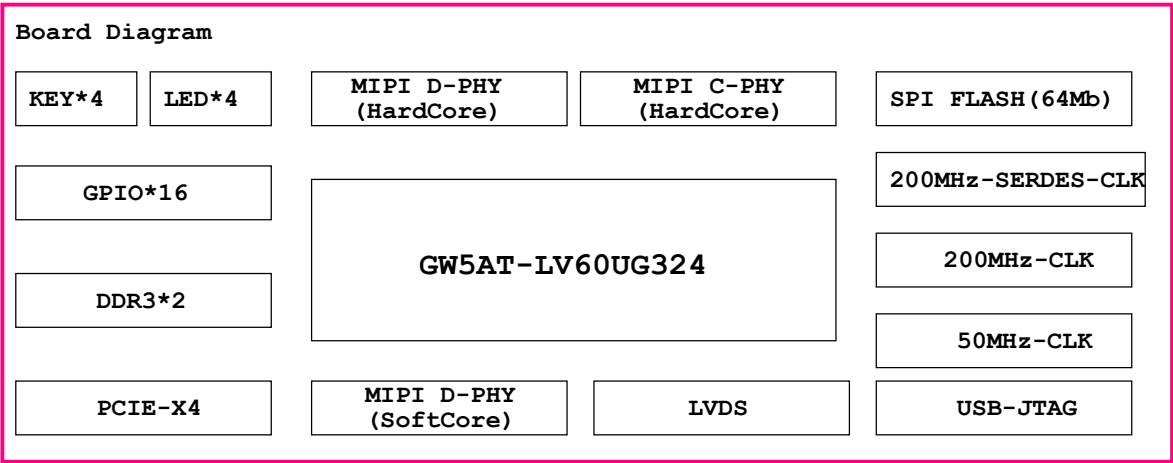
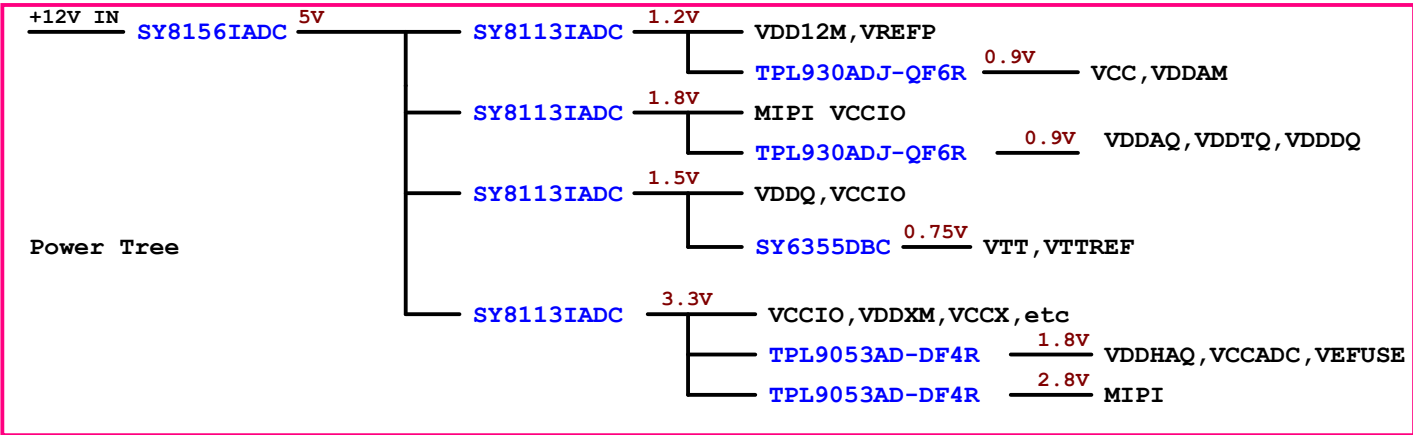
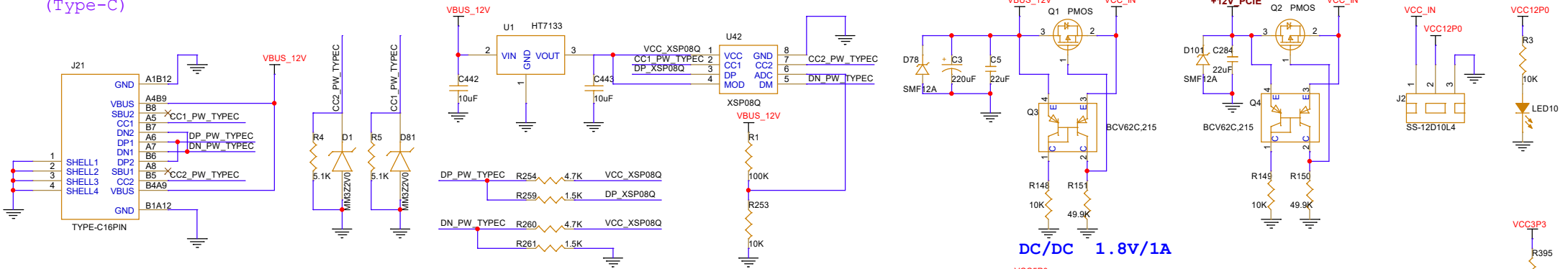


SCHEMATIC	CONTENT	PAGE
COVER PAGE	COVER PAGE	1
POWER	DC/DC , LDO , INA3221	2-3
FPGA	FPGA-GPIO&CONFIG	4
MIPI&LVDS	MIPI , LVDS	5
PCIE	PCIE-X4	6
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USB-JTAG	USB-JTAG , GPIO , KEY , LED	8
FPGA	FPGA-POWER	9

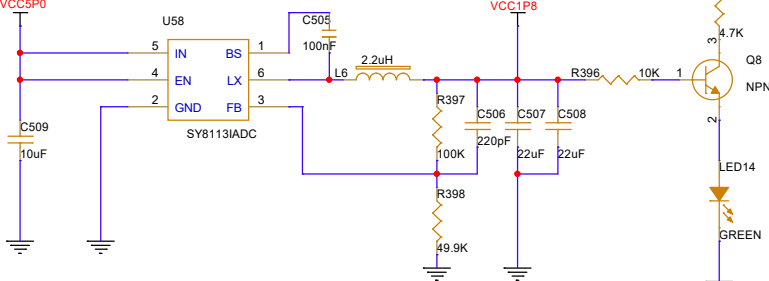


POWER

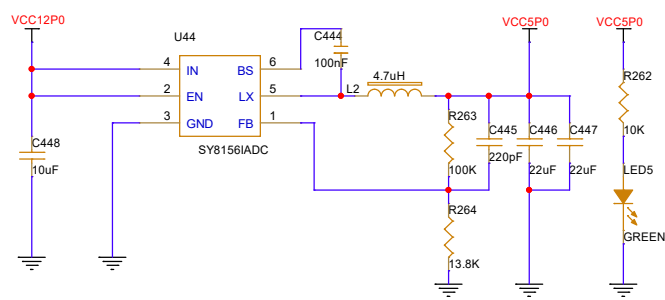
POWER IN
(Type-C)



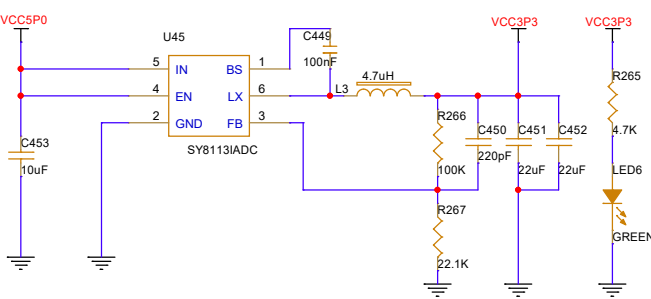
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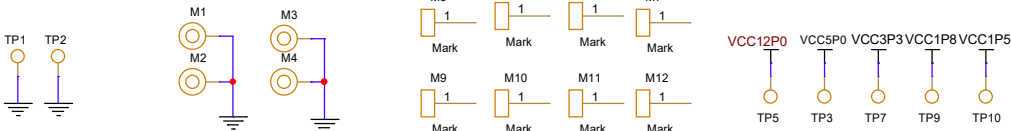
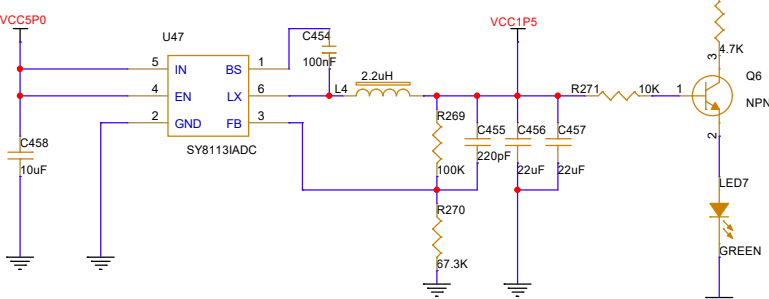
DC/DC 5V/4A



DC/DC 3.3V/1.5A

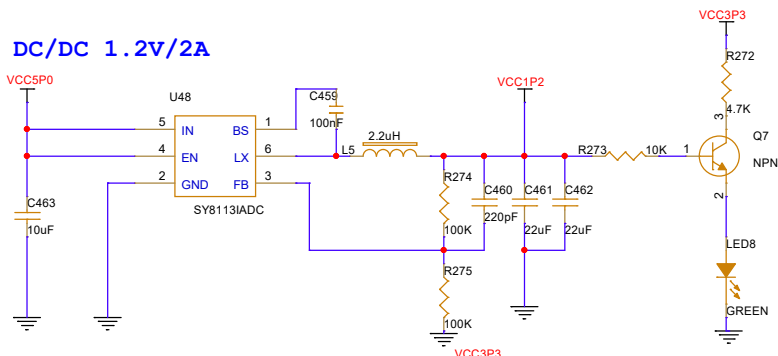


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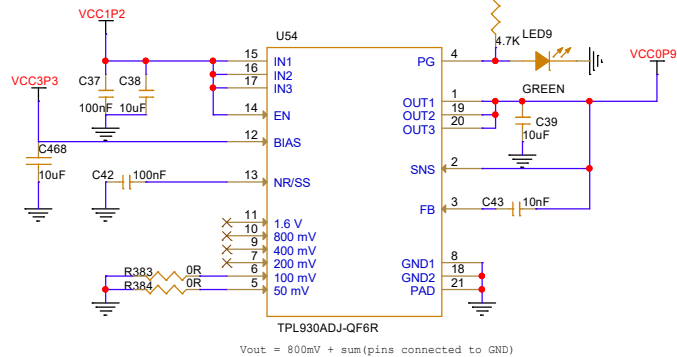


GOWIN 芯云		
Title		
DK_PCIE_GW5AT-LV60UG324		
Size		
A3	Document Number	Rev
POWER		1.1
Date: Tuesday, March 17, 2026		
Sheet 2 of 9		

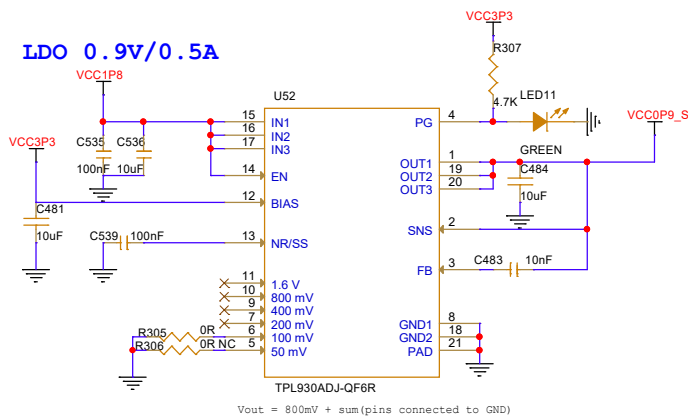
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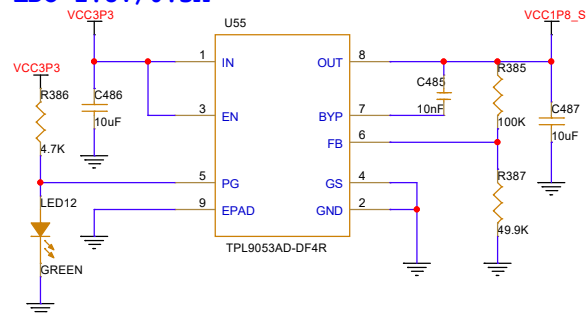
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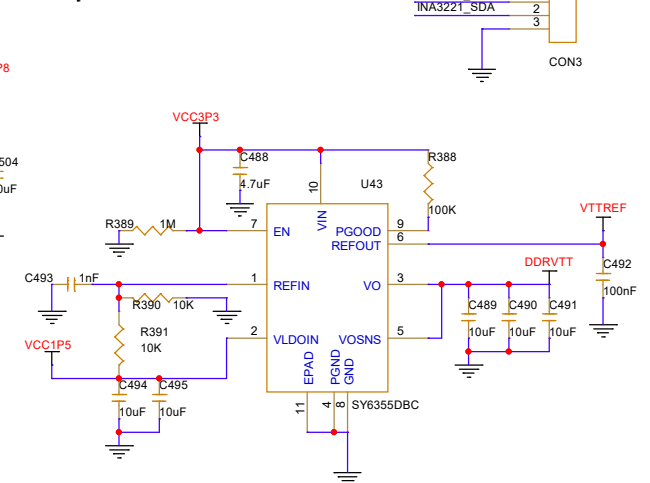
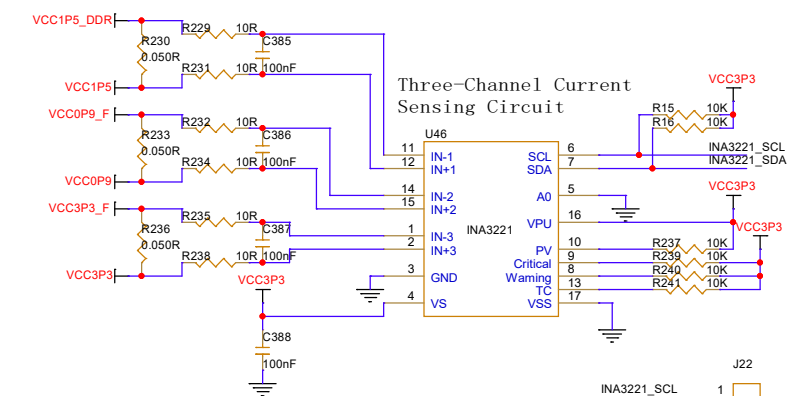
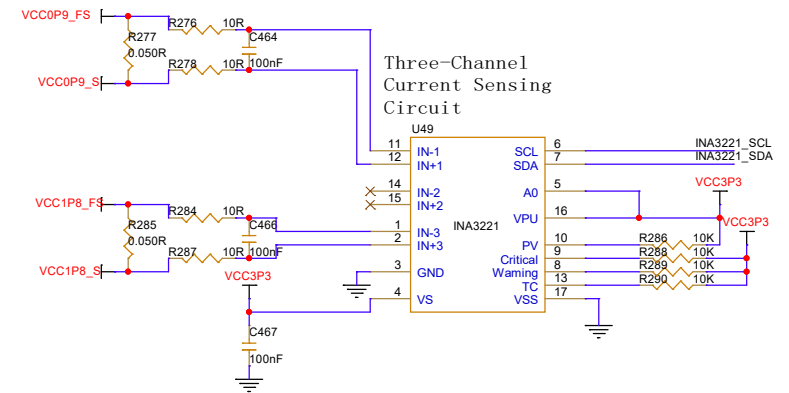
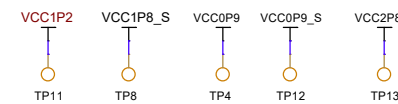
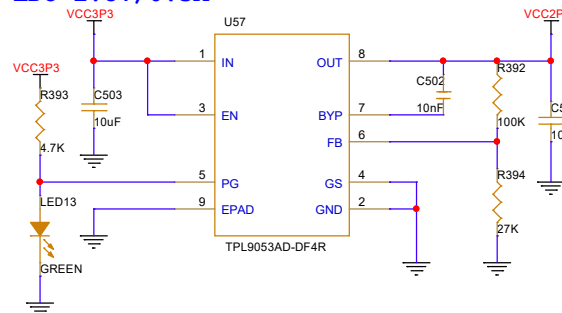
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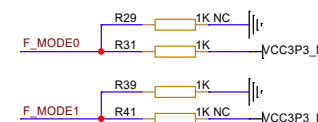
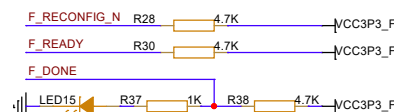
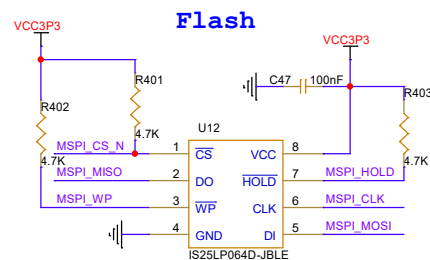
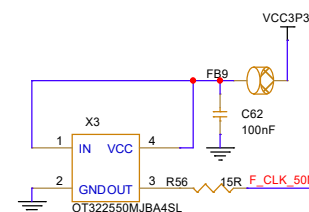
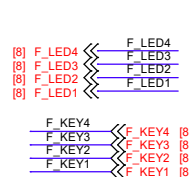
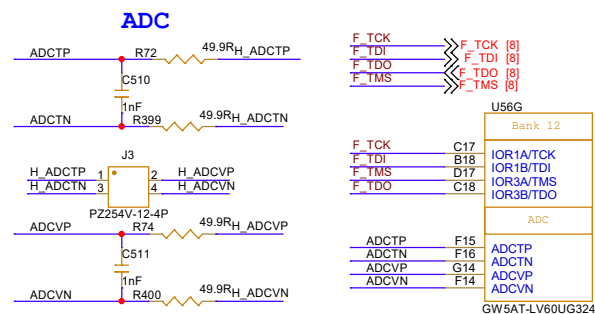
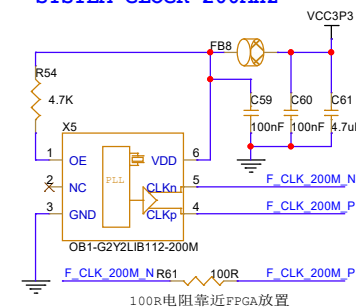
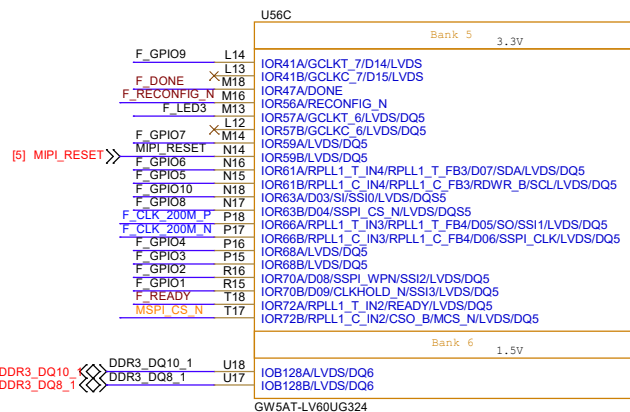


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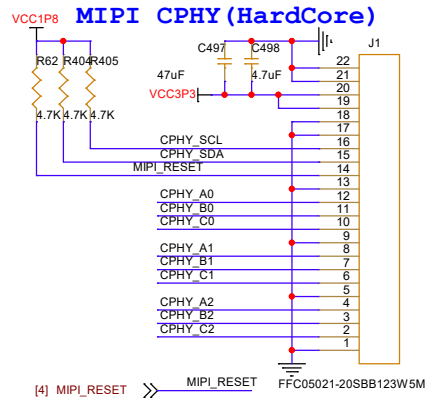
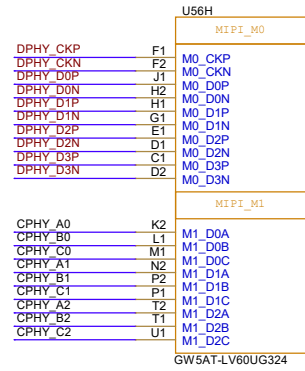


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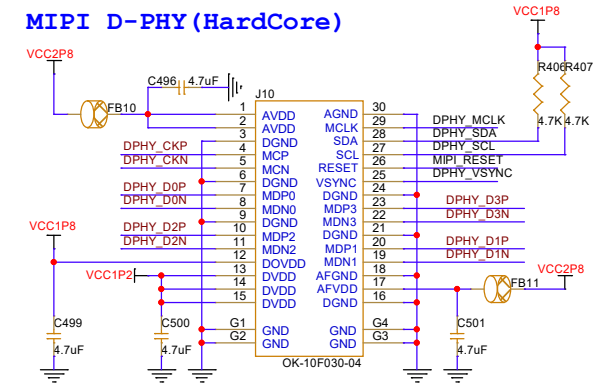




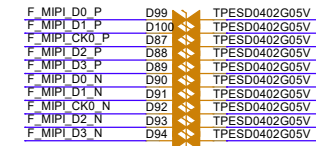
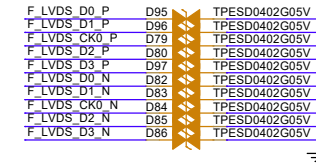
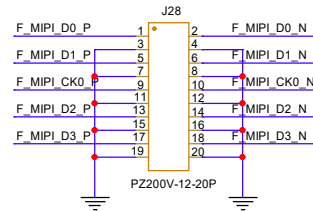
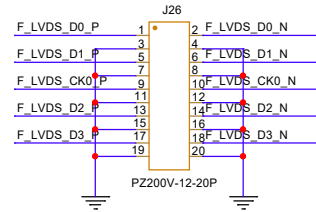
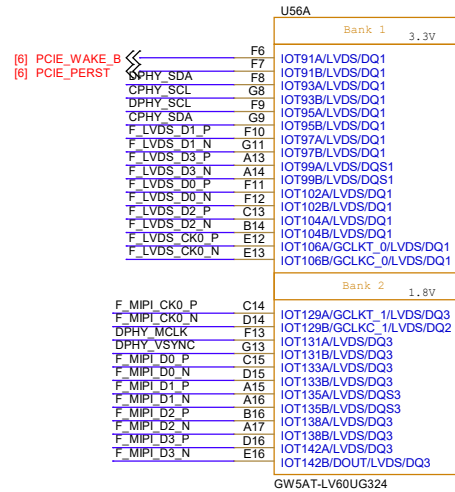
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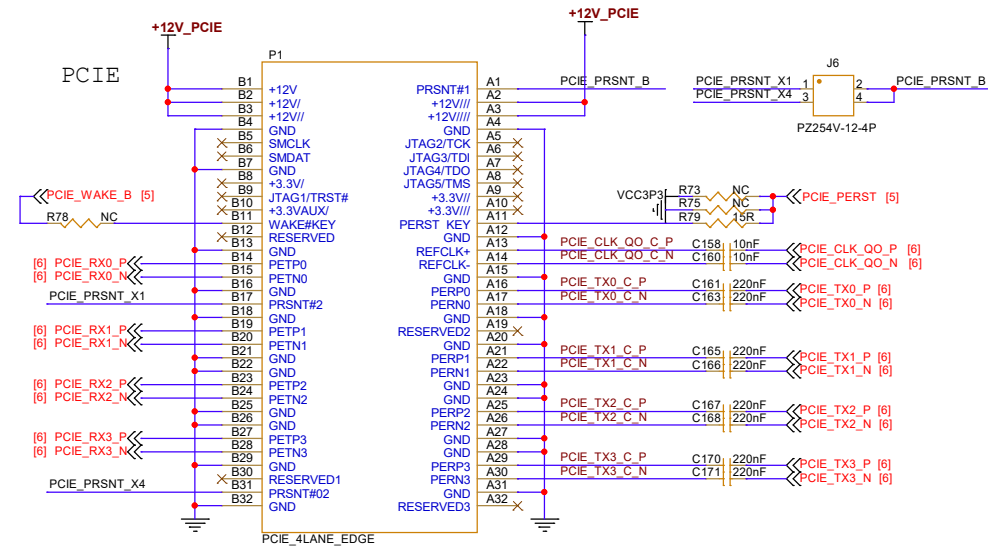
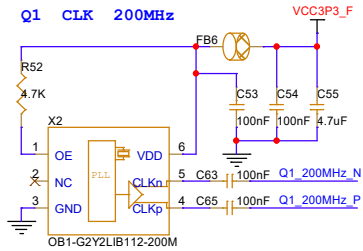
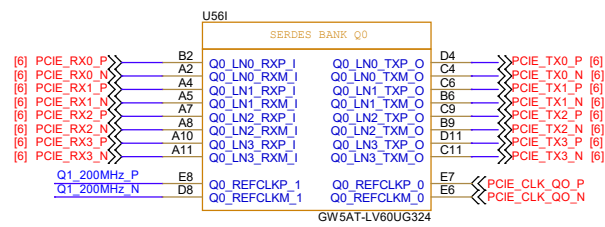
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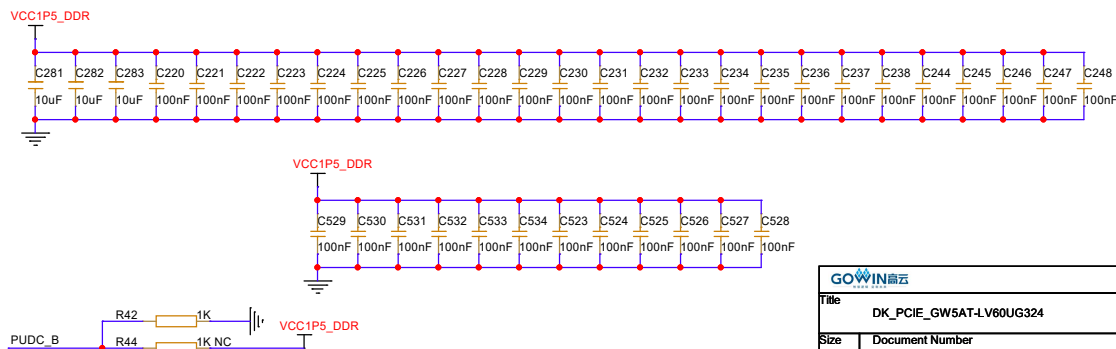
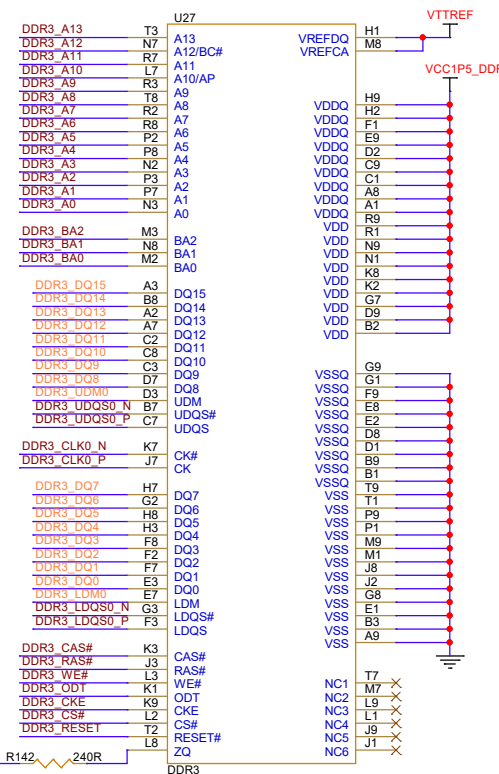
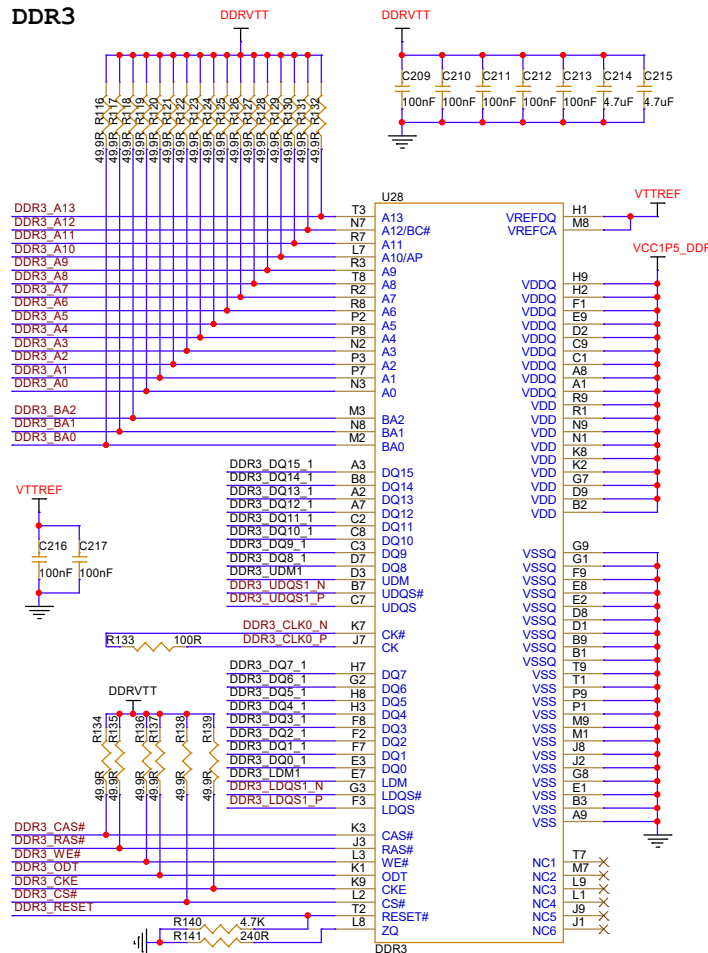
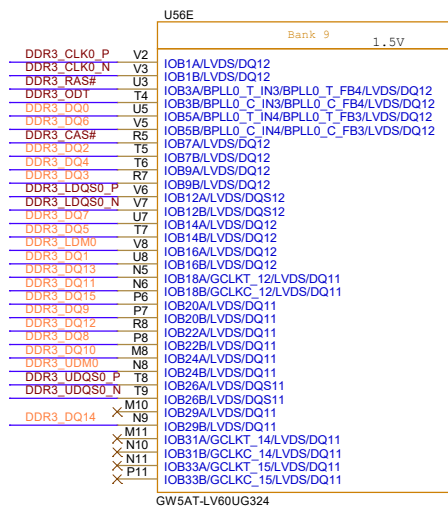
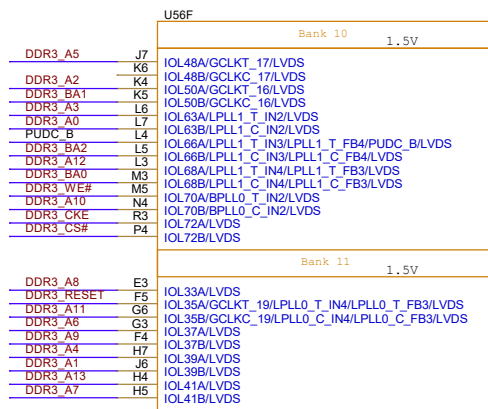
MIPI (SoftCore) & LVDS



PCIE-X4

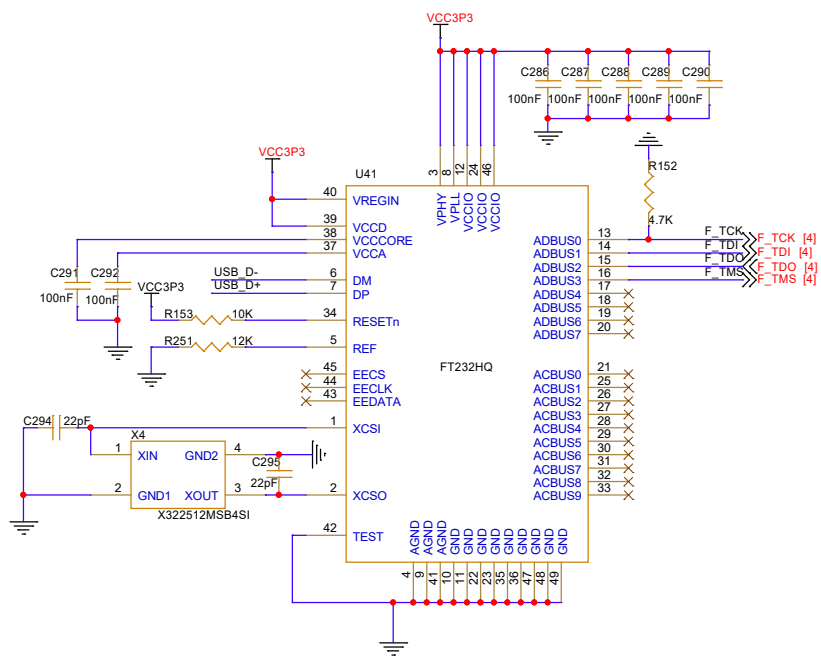
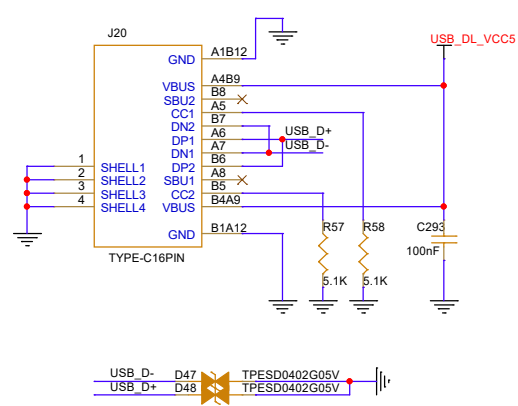


DDR3



USB<->JTAG (FPGA Download)

Type-C



KEY&LED

