



Gowin SDIO SDR104 Slave Controller IP

Release Note

RN774-1.0E, 05/07/2021

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Revision History

Date	Version	Description
05/07/2021	1.0E	Initial version published.

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About This Release

This release includes Gowin SDIO SDR104 Slave Controller IP user guide and reference design.

Gowin SDIO SDR104 Slave Controller IP user guide and reference design are available at Gowin Website. Take specific parameters as an example for the reference design, and it can be used for simulation, synthesis, placement and routing after adding user design.

Overview

This document describes Gowin SDIO SDR104 Slave Controller IP and provides you with a general SDIO Slave interface supporting SDR104. You can integrate this IP to SDIO device design to implement the functions of SDIO Device. It can be integrated to devices that require SDIO Slave and this connection can be applied to a wide range of applications.

Table 1 Gowin SDIO SDR104 Slave Controller IP Overview

Gowin SDIO SDR104 Slave Controller IP	
Delivered Doc.	
Design Files	Verilog (encrypted)
Reference Design	Verilog
Test Bench	Verilog
Test and Design Flow	
Synthesis Software	GowinSynthesis [®]
Application Software	Gowin Software (V1.9.7.03Beta)

Documents

This IP release file contains Gowin SDIO SDR104 Slave Controller IP user guide and release note, which are listed below:

Table 2 Gowin SDIO SDR104 Slave Controller IP Documents

Documents	Description
IPUG774, Gowin SDIO SDR104 Slave Controller User Guide	PDF
RN774, Gowin SDIO SDR104 Slave Controller Release Note	PDF

IP Support

Since the requirements of user design and the maximum frequency are different, the reference design does not include FPGA specific location (.cst) and timing constraints (.sdc) documents, and you can determine the location and timing constraints on your own.

For the customized IP design and support, please contact Gowin sales and support line.

Website: www.gowinsemi.com

E-mail: support@gowinsemi.com

