



Gowin Fixed Point Divider

Release Note

RN523-1.1E, 2020-02-11

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Revision

Date	Revision	Description
08/30/2018	1.0E	Initial version released.
02/11/2020	1.1E	● IP name updated; ● Supported devices updated.

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About This Release

This release includes Gowin Fixed Point Divider IP User Guide.

Gowin Fixed Point Divider IP User Guide can be downloaded from Gowin's official website. The reference design has been configured with specific parameters, which can be used for simulation and instantiation of the synthesis, placement and routing after user design.

Overview

Gowin Fixed Point Divider IP completes signed decimal division with fixed point, where data width and decimal precision can be adjusted as needed.

Table 1 Overview of Gowin Fixed Point Divider IP

Gowin Fixed Point Divider	
IP core application	
Chip supported	Gowin all devices
Delivery document	
Designing documents	Verilog (encrypted)
Reference design	Verilog
TestBench	Verilog
Test design flow	
Integrated software	Synplify_Pro
Application	Gowin YunYuan

Document

The released IP manual is listed in the table below. You can download the PDF version at Gowin website or read online.

Table 2 Gowin Fixed Point Divider User Guide Document

Document	Use
Gowin Fixed Point Divider User Guide.pdf	PDF

IP Support

Because the user design and the highest frequency requirements are different, the reference design does not have associated FPGA-specific location (.cst) and timing constraints (.sdc), and the user can determine it as needed.

For customized IP design and support, please contact the Gowin Sales and Support Hotline.

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