



Gowin SPI Master and Slave Reference Design

Release Note

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Revision History

Date	Version	Description
12/23/2018	1.0E	Initial version published.
03/28/2019	1.1E	Supported products updated.
05/08/2019	1.2E	Changed AXI interface to SRAM interface.
07/17/2019	1.3E	Interface configuration added.
09/25/2019	1.4E	SPI Master released as an IP; SPI Slave released as an open source reference design.

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About This Release

This release includes Gowin SPI Master IP and Gowin SPI Slave reference design.

Gowin SPI Master IP and Slave reference design are available at Gowin Website. It can be used for simulation and instantiation of the overall synthesis, placement and routing after user design.

Feature and Enhancement Summary

The released IP functions and enhancement items are as follows:

Function	Description
Front-end synthesis tool: SynplifyPro, O-2018.09G-SP1-1-Beta1	
Supports SPI Master	Register access, data read/write, etc.
Back-end place and routing tool: GoWinYunYuan_V1.9.1.01Beta	
Enhancements	–
Devices Supported	GW1N-1 GW1N-2 GW1N-2B GW1N-4 GW1N-4B GW1N-6 GW1N-9 GW1N-1S GW1NR-4 GW1NR-4B GW1NR-9 GW1NS-2 GW1NS-2C GW1NSR-2 GW1NSR-2C GW1NZ-1 GW2A-18 GW2A-55 GW2AR-18

Platform Supported

The corresponding software of the released IPs supports the following platforms:

Windows	Windows 7 Ultimate edition (64-bit)
Linux	Red hat Enterprise Linux 6/7 (64-bit)

Documents

The released IP manuals are listed in the table below. You can download the PDF manuals at Gowin website or read online.

Documents	Access
Gowin SPI Master and Slave User Guide	Online, PDF
Gowin SPI Master and Slave Reference Design Release Note	Online, PDF

