



Gowin SDIO Slave Controller **Release Note**

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Revision History

Date	Version	Description
11/08/2019	1.0E	<ul style="list-style-type: none">● SDIO Slave Controller supported by IP Core Generator;● SDIO Slave Controller reference design released;● User guide released.

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About This Release

This release includes Gowin SDIO Slave Controller user guide and reference design.

Gowin SDIO Slave Controller IP user guide and reference design are available at Gowin Website. Taking specific parameters as an example for the reference design, which can be used for simulation and instantiation of the overall synthesis, placement and routing after user design.

Overview

Gowin SDIO Slave Controller realizes the function of SDIO Slave Controller, which is used for data exchange with SDIO Host.

Table 1 Overview of Gowin SDIO Slave Controller IP

Gowin SDIO Slave Controller IP	
IP Core Application	
Supported Devices	GW1N-4, GW1N-4B, GW1N-6, GW1N-9, GW1NR-4, GW1NR-4B, GW1NR-9, GW1NS-4, GW1NS-4C, GW1NSR-4, GW1NSER-4C, GW1NRF-4B, GW2A series, GW2AR series
Delivered Doc.	
Design Files	Verilog (encrypted)
Reference Design	Verilog
TestBench	Verilog
Test and Design Flow	
Synthesis Software	Synplify_Pro
Application Software	Gowin YunYuan (V1.9.2.02Beta)

Document

The software release file contains Gowin SDIO Slave Controller IP user guide. As listed below:

Table 2 Gowin SDIO Slave Controller IP Documents

Documents	Usage
Gowin SDIO Slave Controller User Guide.pdf	PDF

IP Support

Since the requirements of user design and the highest frequency are different, the reference design does not include associated FPGA specific location (.cst) and timing constraints (.sdc) documents, which user can determine on his own.

For the customized IP design and support, please contact Gowin sales and support line.

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