Gowin I3C SDR IP Reference Design

Release Note

RN508-1.2E,09/28/2018

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Revision History

Date	Version	Description
01/17/2018	1.0E	Initial version.
09/28/2018	1.1E	IP name modified.
11/09/2018	1.2E	The supported products modified.

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About This Release

This release includes the Gowin I3C SDR IP reference design and the support of Gowin I3C SDR IP generation by using IP Core Generator.

Gowin I3C SDR IP reference design is available at Gowin Website. It can be used for simulation and instantiation of the overall synthesis, placement and routing after user design.

Function

The released IP functions and improvement items are as follows:

Function	Description
Front-end synthesis tool: SynplifyPro, N-2018.09G- Beta2	
Support I3C SDR IP	Register access, dynamic address allocation, read - write data, in-band interrupt, hot access, etc..
Back-end place and routing tool: GoWinYunYuan_V1.8.0Beta	
Improvement Item	–
Devices Supported	GW1N series GW1NR series GW1NS series GW2A series GW2AR series

Platform Supported

The corresponding software of the released IPs supports the following platforms:

Windows	Windows 7 Ultimate edition（64-bit）
Linux	Red hat Enterprise Linux 6/7（64-bit）

Document

The released IP manuals are listed in the table below. You can download the PDF manuals at Gowin website or read online.

Document	
Gowin I3C SDR IP User Guide	Online, PDF
Gowin I3C SDR IP Reference Design Release Note	Online, PDF

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