



Gowin Controller Area Network (CAN) **Release Note**

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Revision History

Date	Version	Description
01/11/2019	1.0E	1. Gowin CAN Controller IP supported; 2. Gowin CAN Controller IP reference design released; 3. Gowin CAN Controller IP user guide released.
06/28/2019	1.1E	1. APB, Wishbone, and REG bus interface added; 2. IP Core Generator supports APB, Wishbone and REG bus interface option; 3. APB, Wishbone and REG bus interface reference design released; 4. User guide updated.

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About This Release

This release includes Gowin CAN Controller IP user guide and reference designs.

Gowin CAN Controller IP user guide and reference design are available at Gowin Website. Take specific parameters as an example for the reference design, which can be used for simulation and instantiation of the overall synthesis, placement and routing after user design.

Overview

Gowin CAN Controller IP is used to receive the data from microprocessor units and send to the CAN transceiver.

Table 1 Gowin CAN Controller IP

Gowin CAN Controller IP	
IP Core Application	
Supported Devices	GW1N-9, GW1NR-9 GW2A series and GW2AR series
Delivered Doc.	
Design Files	Verilog (encrypted)
Reference Designs	Verilog
TestBench	Verilog
Test and Design Flow	
Synthesis Software	Synplify_Pro
Application Software	GowinYunYuan

Documents

This IP release file contains Gowin CAN Controller IP User Guide, which is listed below:

Table 2 Gowin CAN Controller IP Documents

Documents	
Gowin CAN Controller IP User Guide	PDF
Gowin CAN Controller IP Release Note	PDF

IP Support

Since the requirements of user design and the highest frequency are different, the reference design does not include associated FPGA specific location (.cst) and timing constraints (.sdc) documents, which the user can determine as required.

For the customized IP design and support, please contact Gowin sales and support line.

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