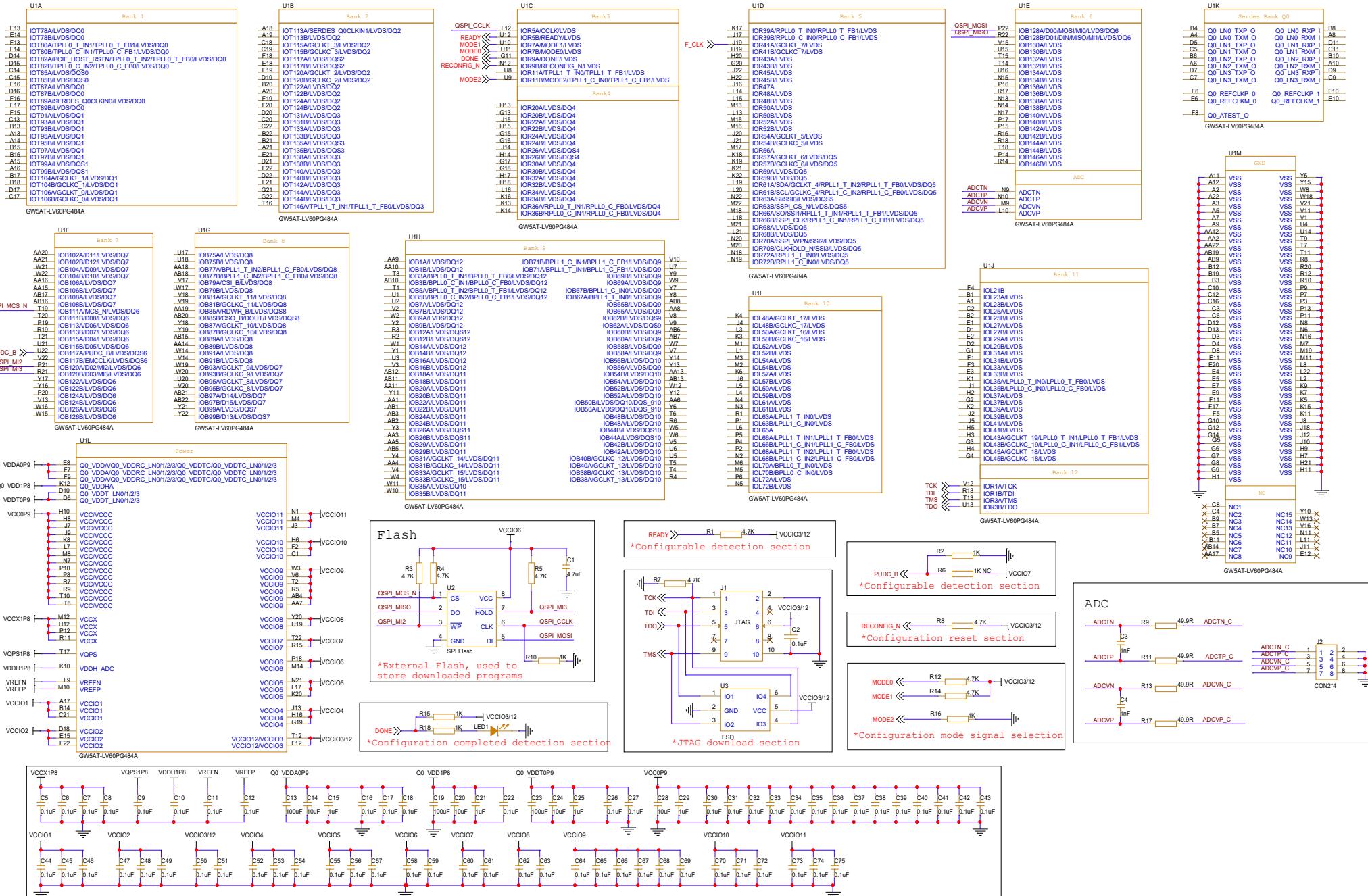


**GW5AT-LV60PG484A**



Notes:  
1.F CLK signal is an external input clock signal.

F<sub>CLK</sub> signal is an external input clock signal. It is recommended that F<sub>CLK</sub> signal be provided through an active oscillator crystal.

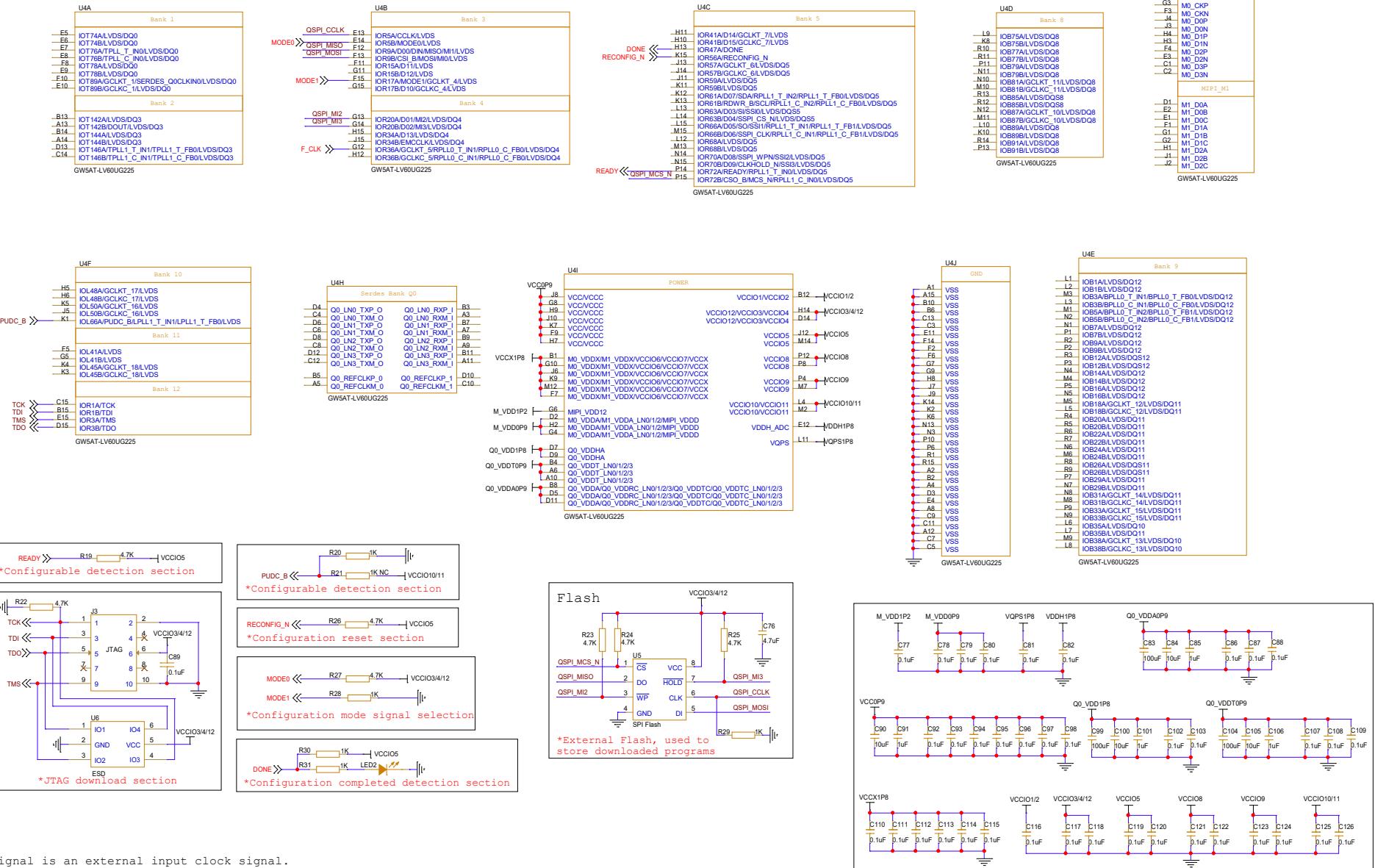
2.External Flash memory is used to store downloaded programs.

4. VCC<sub>core</sub> voltage requires a large current, so it is recommended to supply power separately.

4. VCC core voltage requires a large current, so it is recommended to supply 5. The MODE pin is the GowinCONFIG configuration mode selection signal.

Title <b>GOWIN Minimum System Diagram</b>		
Size C	Document Number GW5AT-LV60PG484A	Rev 1.0.1
Date Thursday, April 11, 2024	Sheet 1	of 2

# GW5AT-LV60UG225



Title		GOWIN Minimum System Diagram	
Size	Document Number	Rev	1.01
C	GW5AT-LV60UG225	Date	Thursday, April 11, 2024
Sheet	2	of	2